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Prioris LX Servers Product Change Information

Initial Release:	May 1997
Current Release	Jun 1998

This document details the following for the Prioris LX Servers product change information:

- Introduction
- Product Change Summary
- BIOS Release Summary
- Product Change Release Notes
- BIOS Change Release Notes
- Customer Impact Ratings
- Future Releases

Introduction

This document is meant to provide customers with a synopsis of product changes. The change information enables customers to evaluate the impact any change has on their environment. All proposed product changes are appraised for their potential impact to daily customer operations. This evaluation includes, but is not limited to such items as safety, emission requirements, functionality, compatibility, reliability, and manufacturing process. Recommendations are then made to customers on whether any action needs to be taken. On occasion, emergency changes may be implemented before they are reflected in this document.

Product Change Summary

The Product Change Summary provides a overview of the "Engineering Change Orders" or ECOs that have been released This matrix provided the part number, part description, date of release, customer impact, and brief description of the change.

At this time, this document only covers Main Logic Board (MLB) and system BIOS changes. Future releases of this document will include ECO information for other major components.

BIOS Release Summary

The BIOS Release Summary is a subset of information from the Product Change Summary. It is meant to provide an overview of just the BIOS Release history This matrix provides the BIOS release number, date of release, customer impact, and brief description. The brief description either denotes a specific change, maintenance release, and/or new feature support. A maintenance release implies that a series of changes or problem fixes were put in place.

Product Change Release Notes

The Product Change Release Notes provide information on each specific change This information is meant to aid the customer in understanding in more detail what has changed and whether they need to take any action The change descriptions include a change, solution if applicable, and a customer recommendation.

Customer Impact Rating

For each product change a customer impact rating is given The customer impact rating can be one of four ratings: High, Medium, Low, or None.

- *HIGH* A "HIGH" rating indicates that this change has some or significant customer impact. This change may affect safety, functionality, compatibility, or reliability
- **MEDIUM** A "MEDIUM" rating indicates that this change may impact some customers or customers using certain configurations. This change will have a minor effect on reliability, functionality, or compatibility.
- *LOW* A "LOW" rating indicates that this change will have a minor impact on customers. This change is typically a product improvement and/or enhancement.
- **NONE** A "NONE" rating indicates that this change has no customer impact. This type of change can range from a part substitution, documentation change, or yield enhancement.

Future Releases

Future releases of this document will incorporate information for other major system components. BIOS changes will be separated from the Main Logic Board (MLB) changes Historically, BIOS releases were tied to MLB revision therefore a change in BIOS version would change the MLB revision In the future, a BIOS changes will be released independently and not affect the MLB revision.

Systems Supported

The information outlined in this document covers the following system variants:

System Variant	Description
5150	Pentium 150 MHZ
5200	Pentium 200 MHZ

Product Change Summary

Part Number	Part	Old Rev	New Rev	ECO Number	Date	Customer Impact	Change Description
54-24048-01	PCI/EISA M/B Mod	M03	M03	5424048-TA017	June 97	None	Re-layout for added component
54-24048-01	PCI/EISA M/B Mod.	M03	R03	5424048-TA019	May 97	High	Product fix
54-24048-01	MLB	P03 R03	S03 T03	5424048-TA020	Jan 98	None	Part substitution
54-24048-01	PCI/EISA M/B Mod	S03 T03	U03 V03	5424048-TA021	Feb 98	High	Replace part

BIOS Release Summary

Part	PCI/EISA M/B Module
Revision	M03 to N03
Component	Re-layout to add component
Severity	None
ECO Issue Date	June 97
Type of Notification	Action not required
Change Description	Re-layout

Description:

Severity: N/A

- **1. Description:** Netware 4.11 nwtest read/write error when E5 change from 21-40593-02 to 21-40593-03 on Laguna MLB (54-24048).
- **2. Description:** Relayout to eliminate the following rework instruction.
 - A. De-solder and remove the BNC connector (J1; 12-39672-01).
 - **B.** Remove (don't cut) the post tails of the BNC connector which are:
 - a) The post tail pins are approximately in the middle of the connector.
 - b) They are the outer most pins on the connector.
 - c) A resistance or continuity tester will show 0 ohms between these pins and the shield (outer connector) on the BNC coaxial connector.
 - **C.** Add the adhesive (49-01272-01; ethyl cyanoacrylate gel) with the accelerator on the J1 area of the PCB.
 - D. Insert the BNC connector (12-39672-01) on J1 and solder the two remaining pins.
 - d) The adhesive in step C must be between the board and the BNC connector.
 - E. Solder a wire (91-07560-01, buss 22 awg tin bare to E4) to the spring clip on the BNC connector (connector to chassis) and those that were cut on the back side of the board.
 - **F.** Add 82 ohm resistor (R370; 13-42645-23; 0805 SMD) between the pin 1 of the R38 and the pin 1 of the R44. The 82 ohm resistor is surface mount size 0805. The resistor is soldered to the resistor pins next to T1 (ycl 10f012n).
 - **G.** Add 47pF capacitor (C276; 10-24051-77) between pin 1 of R55 (V V CAS3L) connect to pin 2 of R56 (GND) on side 1.
 - H. Cut etch from J9 (12-14978-02) pin 1 connect to pin 2 on side 1.

Symptom: N/A

Solution:

None Required

Customer Impact/Recommendations:

No customer impact since this change is manufacturing part substitution.

Part	PCI/EISA M/B Module
Revision	P03 to R03
Component	Add component
Severity	High
ECO Issue Date	May 97
Type of Notification	Action may be required
Change Description	Add 4.7K resistor (70-33198-01)

Description:

Severity: High

Description: Delete 21-39151-01 Real-time Clock (qty: 1 EA E48) and add 70-33198-01 RTC assembly with a 4.7K resistor (qty: 1 EA E48)

Part Number	Old Rev	New Rev	Document Number	Old Rev	New Rev
54-24048-01	M03	P03	K-PL-5424048-1-DBP	S	Т
54-24048-01	N03	R03	K-PL-5424048-2-DBP	В	С

Symptom: CMOS checksum error during hardware reset.

Solution:

- 1. Rework RTC, and add one 4.7K resistor between pin 15 and pin 24 of the RTC.
- 2. Reworked RTC is named "RTC assy, with 4.7K resistor" and the new assigned DEC P/N is 70-33198-01.

Customer Impact/Recommendations:

Implement changes with customer

Part	MLB Module
Revision	P03 to S03 and R03 to T03
Component	EISA chip
Severity	None
ECO Issue Date	Feb 97
Type of Notification	Action not required
Change Description	Replace RTC Assy.

Description:

Severity: High

Description: Causes system to hang easily by pushing down the reset button if Laguna MLB uses RTC Assembly 70-33198-01 revision A01.

Symptom: N/A

Solution:

Replace RTC Assembly 70-33198-01 with 70-33198-02.

Customer Impact/Recommendations:

Recommend to change RTC assembly to new reworked RTC assembly 70-33198-02 on Laguna MLB.

Part	MLB Module
Revision	S03 to T03 and U03 to V03
Component	PCI/EISA M/B
Severity	High
ECO Issue Date	July 97
Type of Notification	Action not required
Change Description	21-40885-02 can be substituted with 21-40885-01

Description:

Severity: None

Description: 21-39196-01 and 21-39238-01 are in shortage. There is also a KPL GEN NOTE error.

Part Number	Old Rev	New Rev
54-24048-01	P03	S03
54-24048-01	R03	T03

Symptom: N/A

Solution:

- 1. Add Note: 21-40885-01 and 21-40885-02.
- 2. Delete R294 and add R293
- 3. Del Gen Note 2 (both 21-40884-01 and 21-40885-01 can be substituted by 21-39196-01 and 21-39238-01.

Customer Impact/Recommendations:

No customer impact.